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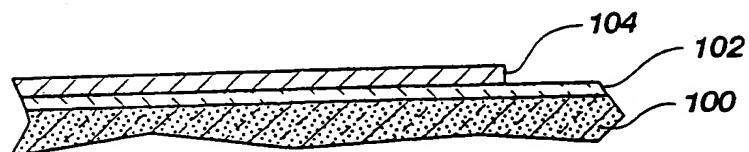


Fig. 1

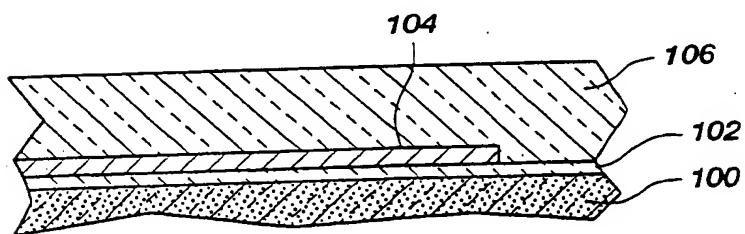


Fig. 2

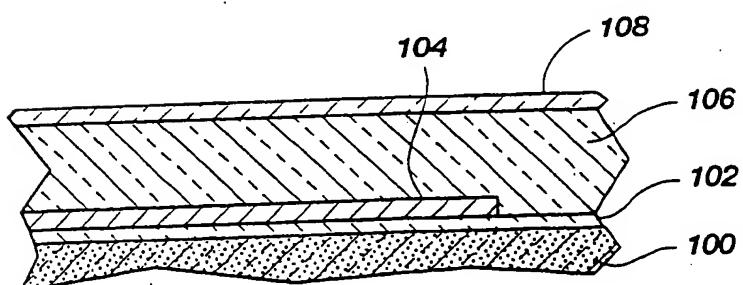


Fig. 3

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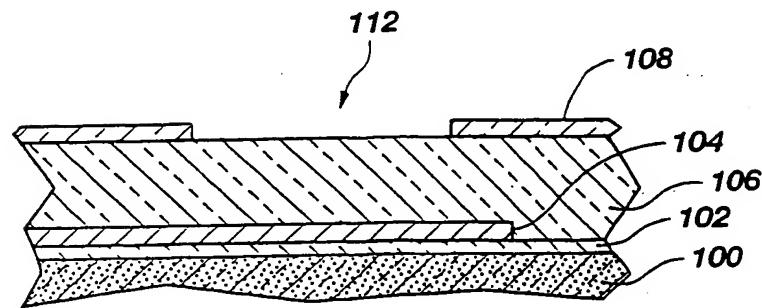


Fig. 4

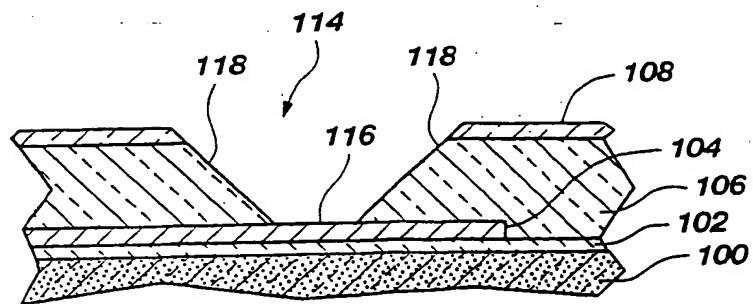


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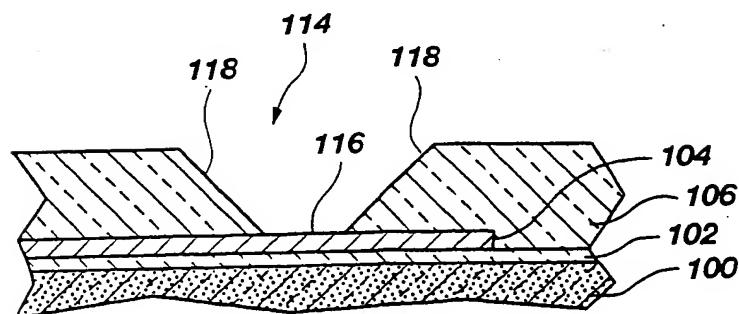


Fig. 6

TITLE: METHODS OF FABRICATING SEMICONDUCTOR
SUBSTRATE-BASED BGA INTERCONNECTION

Inventor: Salman Akram
Serial No.: Not Yet Assigned
Docket No.: 3091.5US

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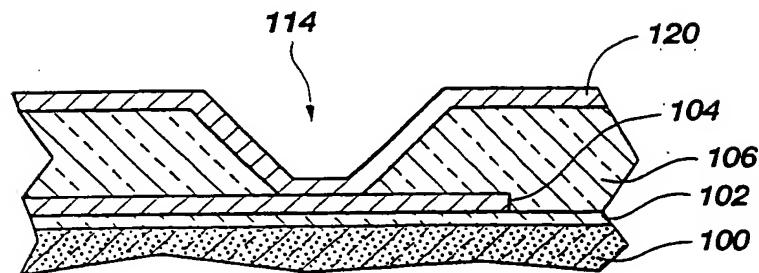


Fig. 7

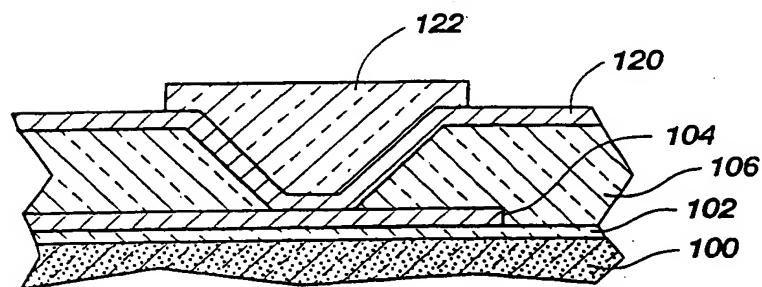


Fig. 8

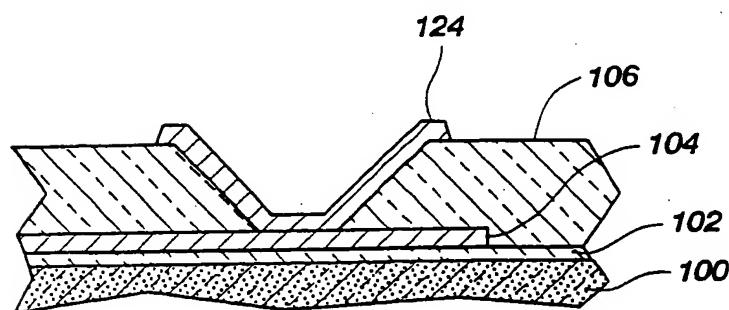


Fig. 9

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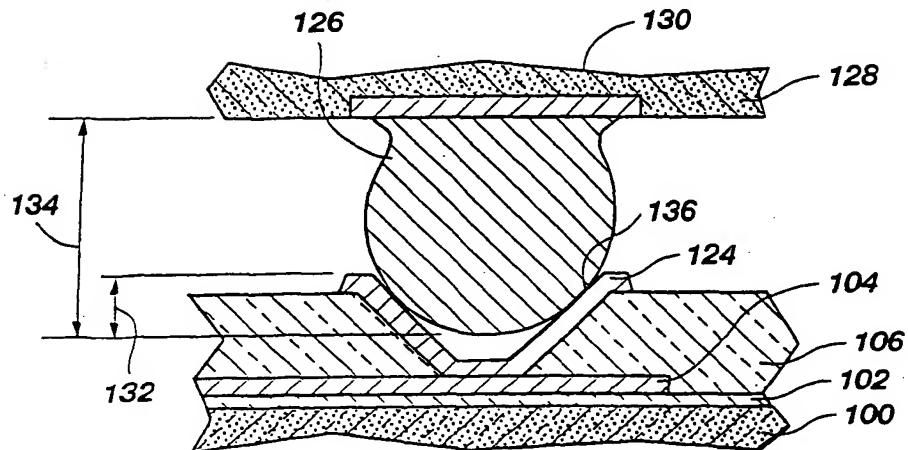


Fig. 10

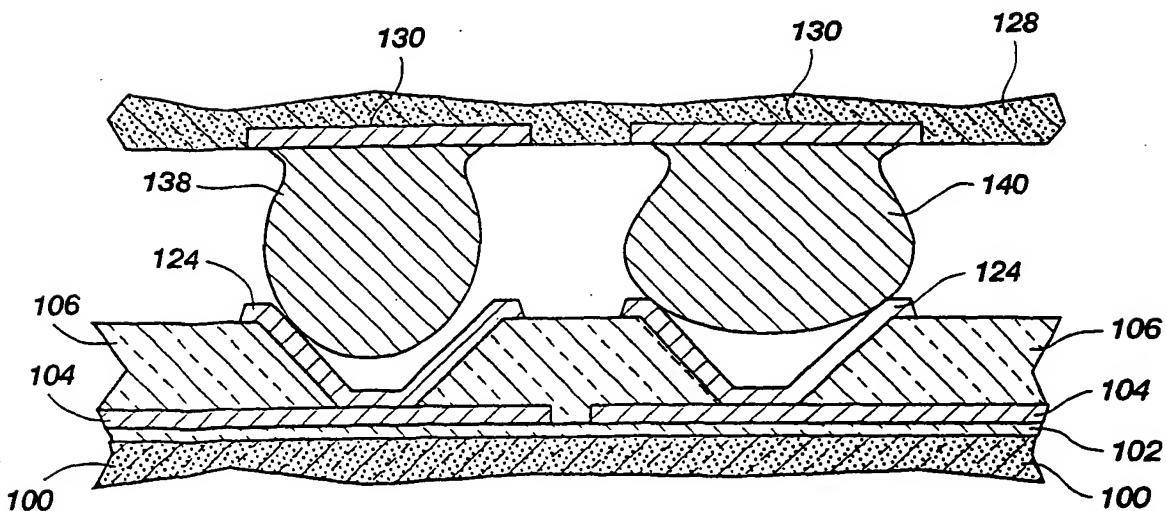


Fig. 11

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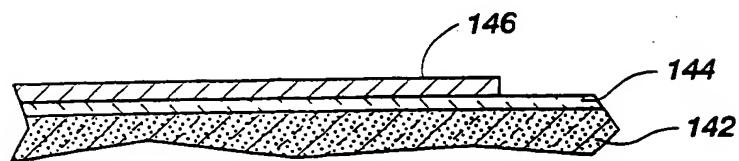


Fig. 12

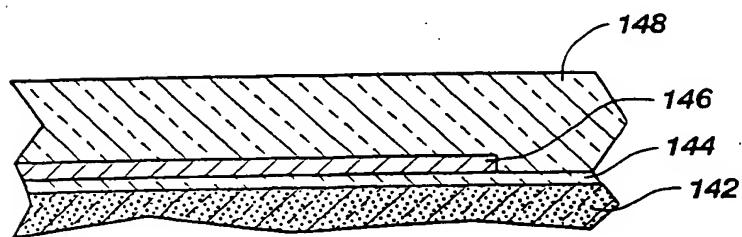


Fig. 13

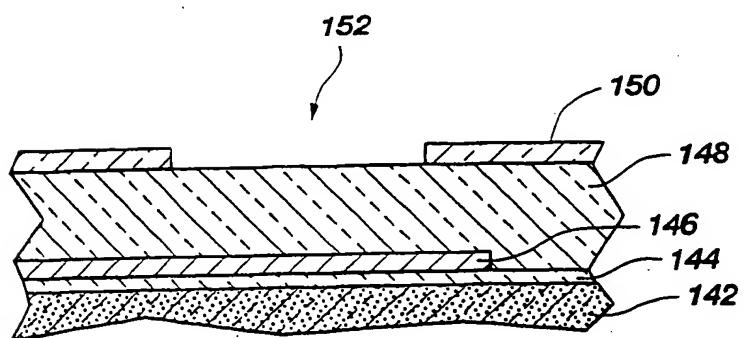


Fig. 14

TITLE: METHODS OF FABRICATING SEMICONDUCTOR
SUBSTRATE-BASED BGA INTERCONNECTION

Inventor: Salman Akram
Serial No.: Not Yet Assigned
Docket No.: 3091.5US

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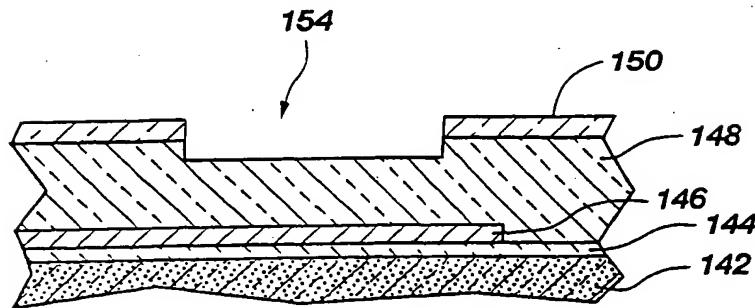


Fig. 15

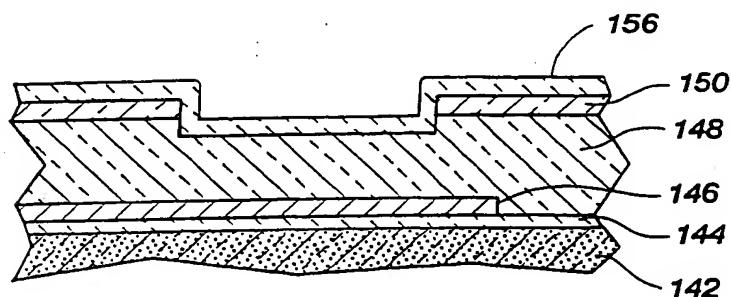


Fig. 16

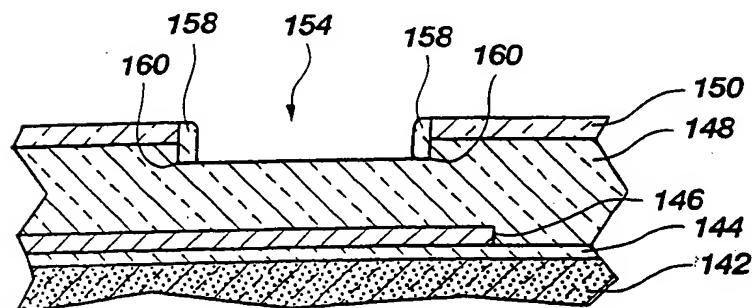


Fig. 17

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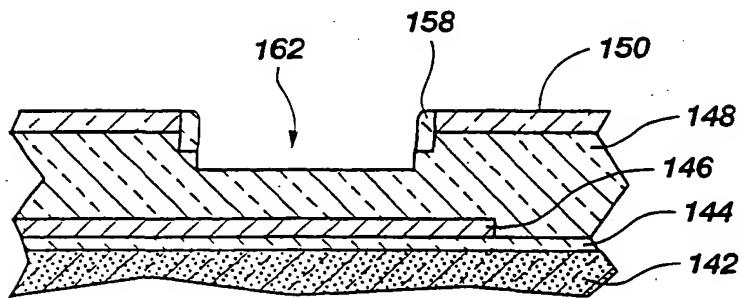


Fig. 18

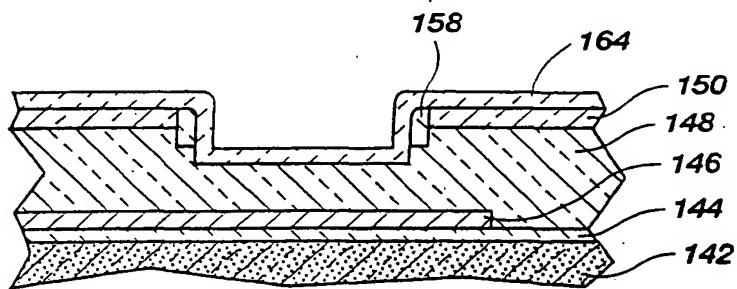


Fig. 19

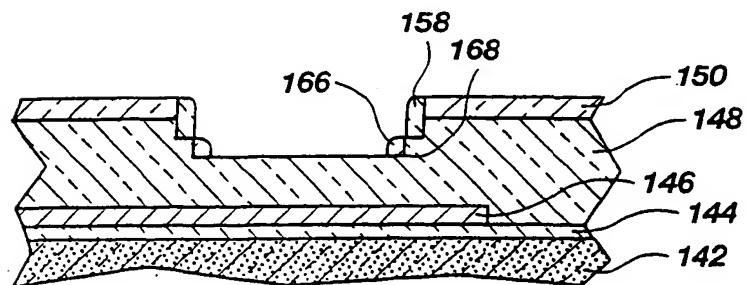


Fig. 20

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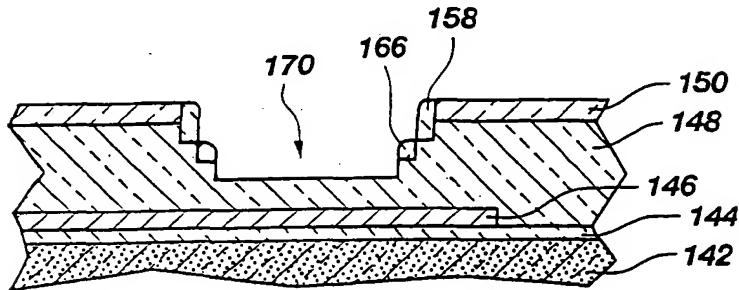


Fig. 21

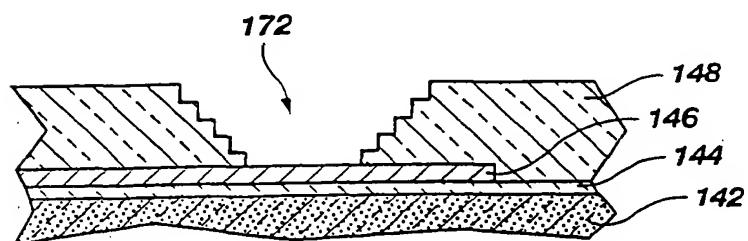


Fig. 22

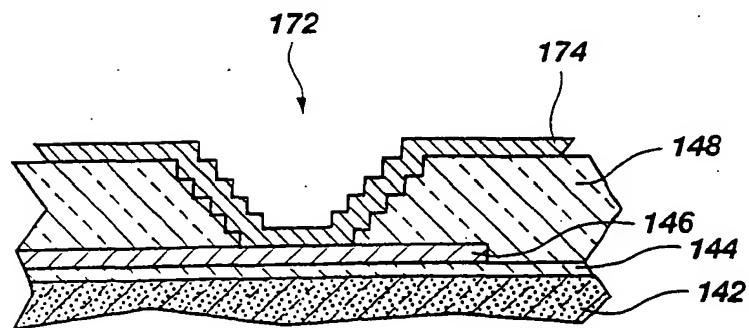


Fig. 23

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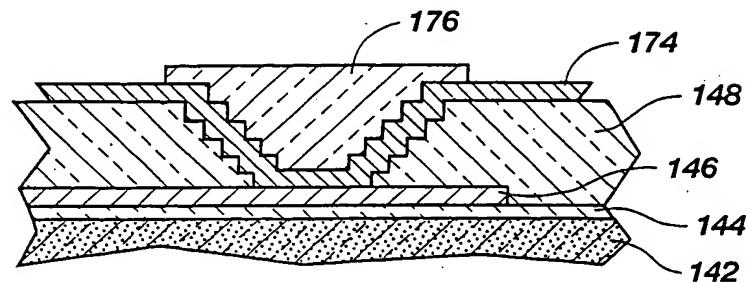


Fig. 24

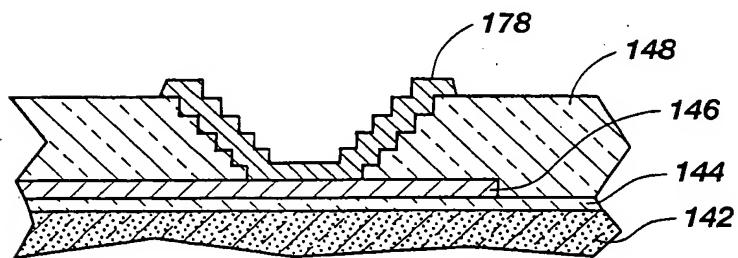


Fig. 25

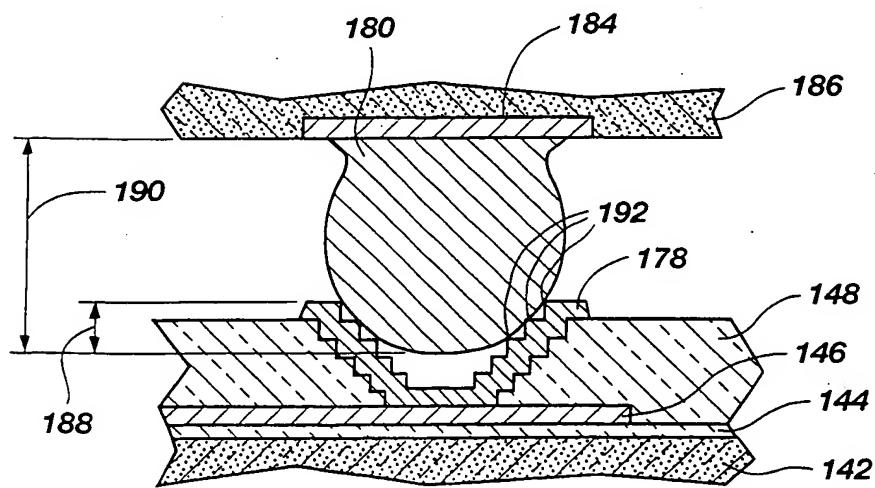
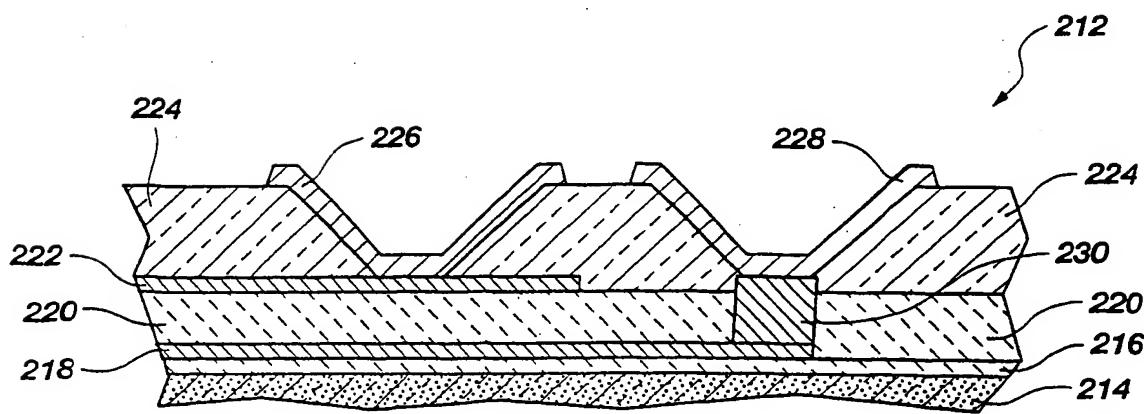
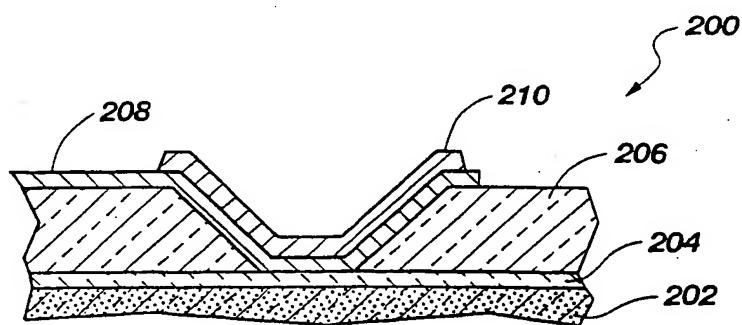
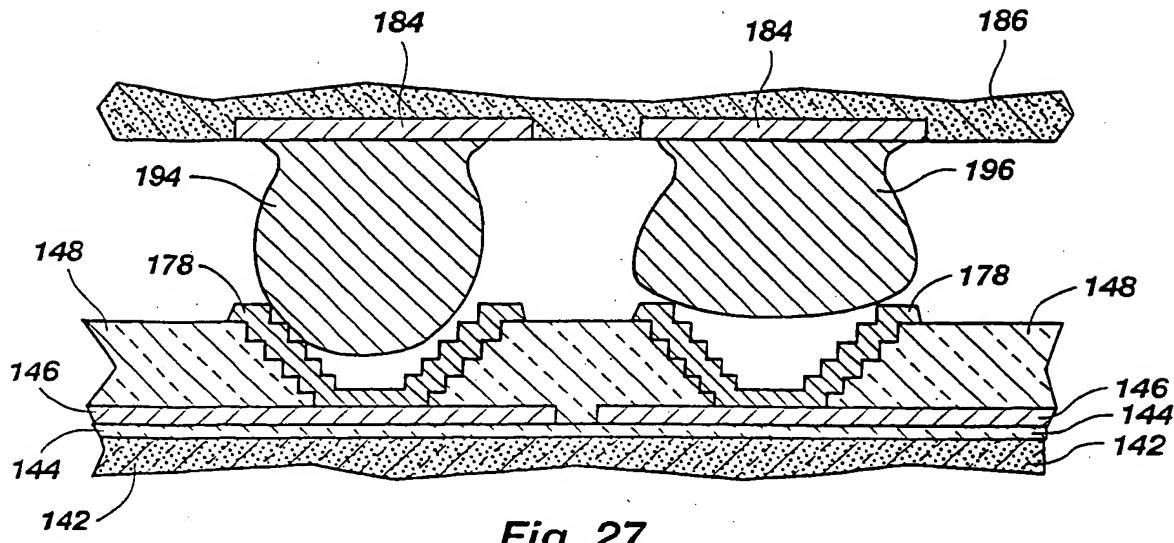


Fig. 26

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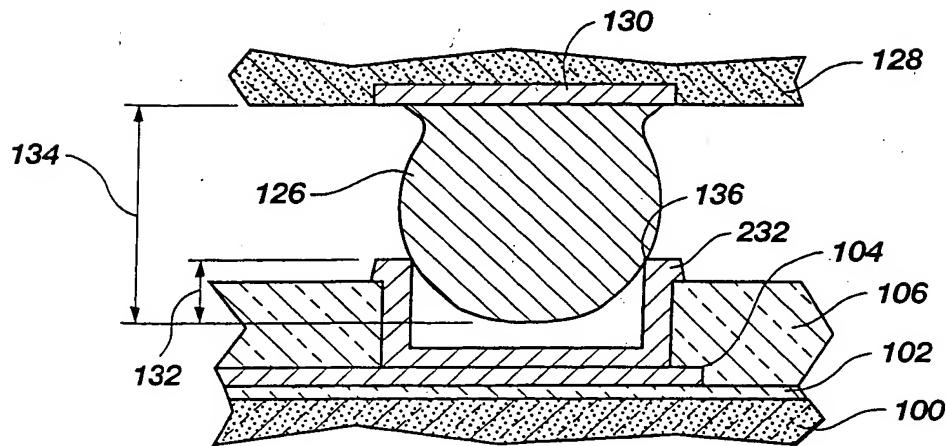


Fig. 30

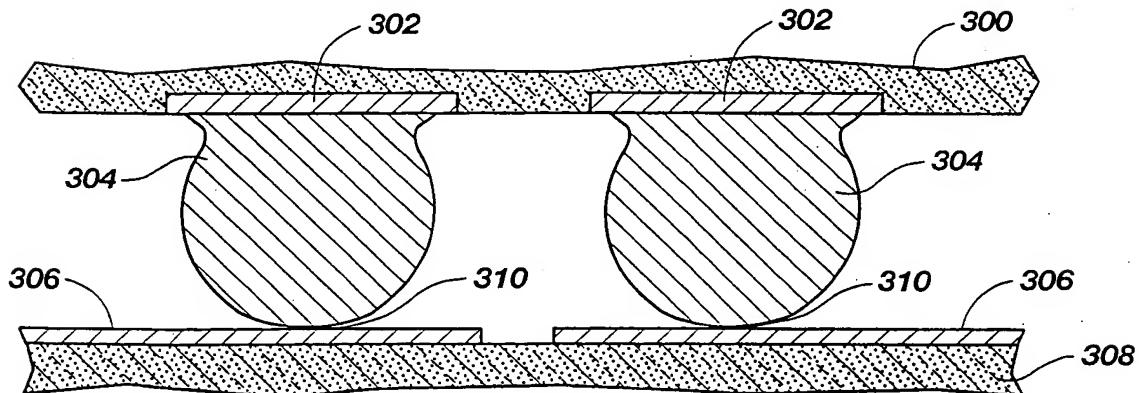


Fig. 31
(PRIOR ART)

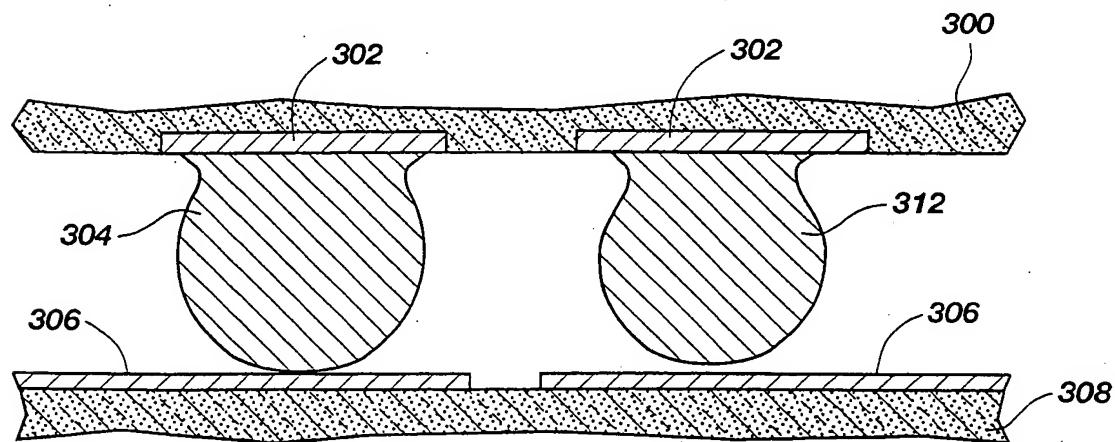


Fig. 32
(PRIOR ART)